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January 17, 2006
VIA FACSIMILE

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Mr. Chang-Hyun Kim
Koryo International Patent & Law Office
3rd Fl., 827-25 Yeoksam-dong
Kangnam-ku, Seoul, 135-080
Republic of Korea

Re: United States Patent Application No. 10/675,336
SEMICONDUCTOR DEVICE WITH RESISTOR PATTERN AND METHOD OF
FABRICATING THE SAME
Korean Priority Document No. 2002-6140, filed October 9, 2002
Your Ref. No.: IE12101-US
Our Doc. No.: 4591-330

Dear Mr. Kim:

We have finally scheduled a phone interview with the Examiner in this case for Monday, January 23, which is the earliest possible date for the Examiner, although we have been trying to have an earlier date.

In that interview we will explain to the Examiner that the Chang reference does not contain a suggestion to combine Chang with the Admitted Prior Art.

Further, depending on the Examiner's response, we will put forth a proposed amendment to claim 1 reciting that a portion of an inner sidewall of the resistor spacer contacts the interlayer dielectric layer unless you disagree. See page 5, lines 15-16 of the present application. We believe this amendment would more clearly distinguish claim 1 from the Chang reference. To enter this amendment we would need to file a Request for Continued Examination.

We will report back to you the results of the phone interview and then will await your instructions on how to proceed with this case including filing an appeal brief. In the meantime, if you have any other suggestions regarding the reasons why there are no suggestions to combine or possible amendment to allow the claims, please let us know.

Lastly, we apologize for the delay and will absorb extension fees resulting from this delay.

Sincerely,

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